

## ***1. Description***

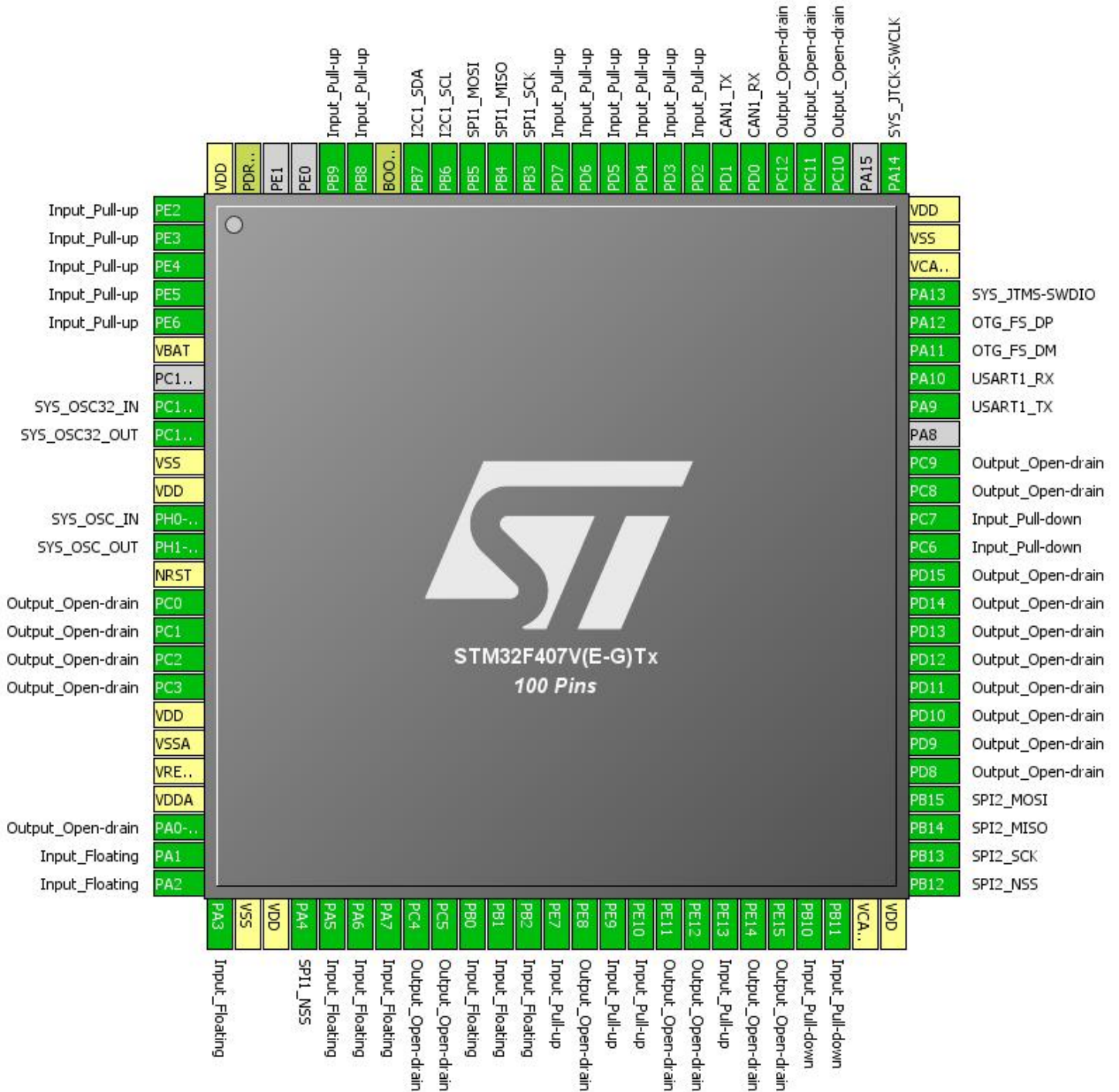
### 1.1. Project

Project Name	STM32F407
Generated with:	MicroXplorer V2.0
Date	07/03/2012

### 1.2. MCU

MCU family	STM32
MCU name	STM32F407V(E-G)Tx
MCU Package	LQFP100
MCU Pin number	100

## 2. Pinout Configuration



### 3. Peripherals Configuration

Peripheral	Mode	Remap	Function	Pin
SPI1	Full-Duplex-Master	1	SPI1_MISO	PB4
			SPI1_MOSI	PB5
			SPI1_SCK	PB3
	With-NSS	0	SPI1_NSS	PA4
SPI2	Full-Duplex-Master	1	SPI2_MISO	PB14
			SPI2_MOSI	PB15
			SPI2_SCK	PB13
	With-NSS	0	SPI2_NSS	PB12
I2C1	I2C	0	I2C1_SCL	PB6
			I2C1_SDA	PB7
USART1	Asynchronous	0	USART1_RX	PA10
			USART1_TX	PA9
CAN1	CAN	1	CAN1_RX	PD0
			CAN1_TX	PD1
OTG_FS	Device-Only(InternalPHY)	0	OTG_FS_DM	PA11
			OTG_FS_DP	PA12
SYS	Serial-Wire	0	SYS_JTCK-SWCLK	PA14
			SYS_JTMS-SWDIO	PA13
	SYS-OSC	0	SYS_OSC_IN	PH0-OSC_IN
			SYS_OSC_OUT	PH1-OSC_OUT
	SYS-OSC32	0	SYS_OSC32_IN	PC14-OSC32_IN
			SYS_OSC32_OUT	PC15-OSC32_OUT

## 4. Pins configuration

Pins	Pos	Functions
PE2 *	1	Input_Pull-up
PE3 *	2	Input_Pull-up
PE4 *	3	Input_Pull-up
PE5 *	4	Input_Pull-up
PE6 *	5	Input_Pull-up
PC14-OSC32_IN	8	SYS_OSC32_IN
PC15-OSC32_OUT	9	SYS_OSC32_OUT
PH0-OSC_IN	12	SYS_OSC_IN
PH1-OSC_OUT	13	SYS_OSC_OUT
PC0 *	15	Output_Open-drain
PC1 *	16	Output_Open-drain
PC2 *	17	Output_Open-drain
PC3 *	18	Output_Open-drain
PA0-WKUP *	23	Output_Open-drain
PA1 *	24	Input_Floating
PA2 *	25	Input_Floating
PA3 *	26	Input_Floating
PA4	29	SPI1_NSS
PA5 *	30	Input_Floating
PA6 *	31	Input_Floating
PA7 *	32	Input_Floating
PC4 *	33	Output_Open-drain
PC5 *	34	Output_Open-drain
PB0 *	35	Input_Floating
PB1 *	36	Input_Floating
PB2 *	37	Input_Floating
PE7 *	38	Input_Pull-up
PE8 *	39	Output_Open-drain
PE9 *	40	Input_Pull-up
PE10 *	41	Input_Pull-up
PE11 *	42	Output_Open-drain
PE12 *	43	Output_Open-drain
PE13 *	44	Input_Pull-up
PE14 *	45	Output_Open-drain
PE15 *	46	Output_Open-drain
PB10 *	47	Input_Pull-down
PB11 *	48	Input_Pull-down
PB12	51	SPI2_NSS
PB13	52	SPI2_SCK

Pins	Pos	Functions
PB14	53	SPI2_MISO
PB15	54	SPI2_MOSI
PD8 *	55	Output_Open-drain
PD9 *	56	Output_Open-drain
PD10 *	57	Output_Open-drain
PD11 *	58	Output_Open-drain
PD12 *	59	Output_Open-drain
PD13 *	60	Output_Open-drain
PD14 *	61	Output_Open-drain
PD15 *	62	Output_Open-drain
PC6 *	63	Input_Pull-down
PC7 *	64	Input_Pull-down
PC8 *	65	Output_Open-drain
PC9 *	66	Output_Open-drain
PA9	68	USART1_TX
PA10	69	USART1_RX
PA11	70	OTG_FS_DM
PA12	71	OTG_FS_DP
PA13	72	SYS_JTMS-SWDIO
PA14	76	SYS_JTCK-SWCLK
PC10 *	78	Output_Open-drain
PC11 *	79	Output_Open-drain
PC12 *	80	Output_Open-drain
PD0	81	CAN1_RX
PD1	82	CAN1_TX
PD2 *	83	Input_Pull-up
PD3 *	84	Input_Pull-up
PD4 *	85	Input_Pull-up
PD5 *	86	Input_Pull-up
PD6 *	87	Input_Pull-up
PD7 *	88	Input_Pull-up
PB3	89	SPI1_SCK
PB4	90	SPI1_MISO
PB5	91	SPI1_MOSI
PB6	92	I2C1_SCL
PB7	93	I2C1_SDA
PB8 *	95	Input_Pull-up
PB9 *	96	Input_Pull-up

\* The pin is affected with an I/O function